

09-703-517

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,741,289 B1
DATED : May 25, 2004
INVENTOR(S) : Bicakci

Page 1 of 7

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The title page showing illustrative figure should be deleted to be replaced with the attached title page.

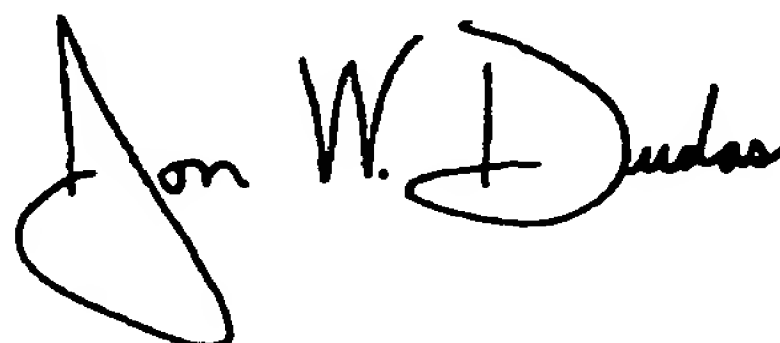
Drawings sheets, consisting of Figs. 1-7, should be deleted to be replaced with the drawings sheets, consisting of Figs. 1-7, as shown on the attached pages.

Title page,

Item [73], Assignee, delete "FAIRCHILD SEMICONDUCTORS, INC." and insert
-- FAIRCHILD SEMICONDUCTOR, INC. --

Signed and Sealed this

Twenty-eighth Day of September, 2004

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is written in a cursive style with a large, looped initial "J".

JON W. DUDAS
Director of the United States Patent and Trademark Office

(12) **United States Patent**
Bicakci

(10) Patent No.: **US 6,741,289 B1**
(45) Date of Patent: **May 25, 2004**

(54) **TECHNIQUE TO STABILIZE THE CHROMINANCE SUBCARRIER GENERATION IN A LINE-LOCKED DIGITAL VIDEO SYSTEM**

(75) Inventor: **Ara Bicakci, San Jose, CA (US)**

(73) Assignee: **Fairchild Semiconductors, Inc., South Portland, ME (US)**

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 631 days.

4,987,491 A	1/1991	Kaite et al.	358/166
4,989,073 A	1/1991	Wagner	358/19
5,132,554 A	7/1992	Hiramatsu et al.	307/269
5,179,438 A	1/1993	Morimoto	358/17
5,280,345 A	1/1994	Uchida et al.	358/37
5,303,061 A	4/1994	Matsumoto et al.	358/320
5,359,366 A	10/1994	Ubukata et al.	348/536
5,497,200 A	3/1996	Otake et al.	348/497
5,557,335 A	9/1996	Oh	348/512
5,796,796 A	8/1998	Wang	375/372
5,877,640 A	3/1999	Geerlings	327/141
5,999,226 A	12/1999	Choi	348/564
6,256,003 B1	7/2001	Tsuchiya et al.	345/87
6,310,653 B1	10/2001	Malcolm, Jr. et al.	348/537
6,363,129 B1	3/2002	Agazzi	375/355

(21) Appl. No.: **09/703,517**

* cited by examiner

(22) Filed: **Oct 31, 2000**

(51) Int. Cl.⁷ **H04N 9/45**

(52) U.S. Cl. **348/505; 348/194; 348/508**

(58) Field of Search **348/194, 505, 348/508, 512, 549; 386/41, 13, 17, 19, 20**

(56) **References Cited**

U.S. PATENT DOCUMENTS

4,370,672 A	1/1983	Ross et al.	358/312
4,639,780 A	1/1987	Willis	358/150
4,694,327 A	9/1987	Demmer et al.	358/19
4,697,207 A	9/1987	Lilley	358/17
4,718,074 A	1/1988	Mannas et al.	375/118

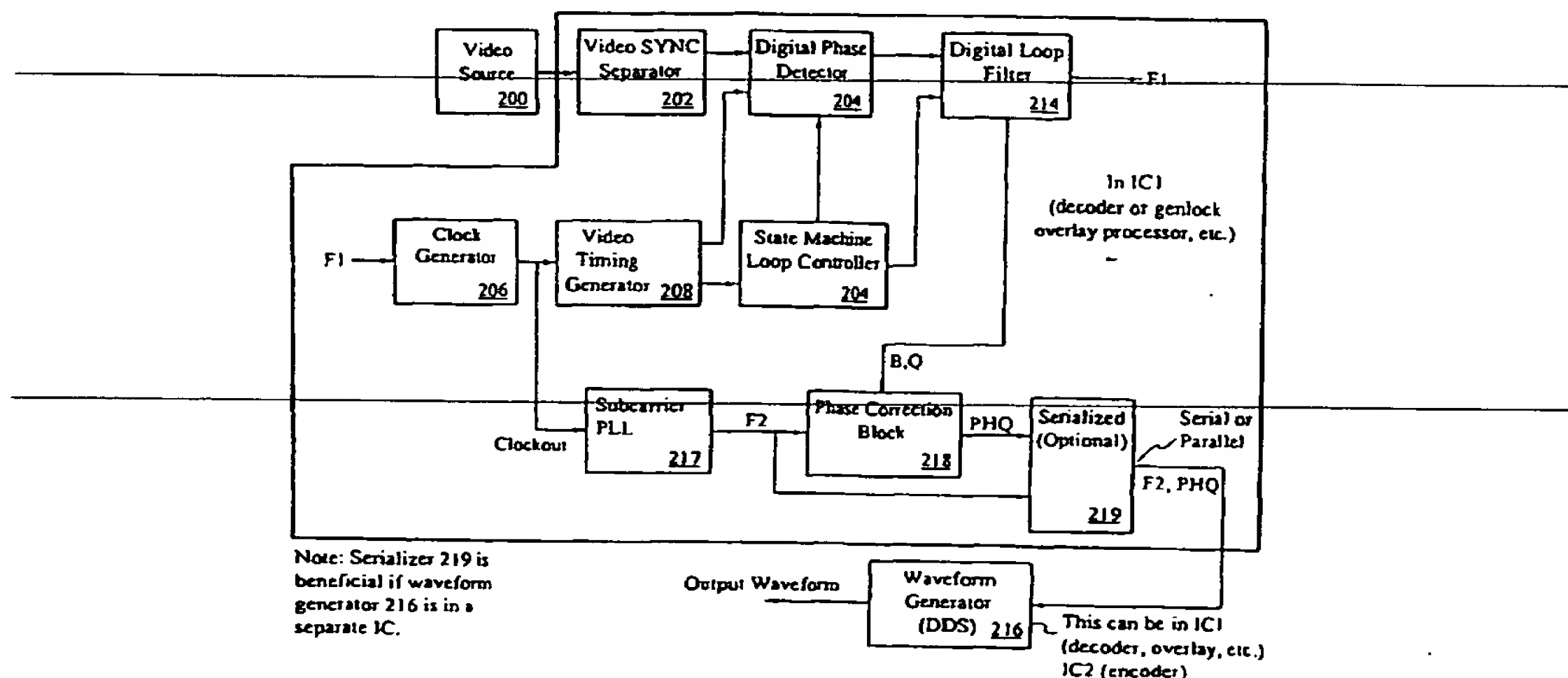
Primary Examiner—Victor R. Kostak

(74) Attorney, Agent, or Firm—Haverstock & Owens LLP

(57) **ABSTRACT**

A technique to stabilize subcarrier generation in a line-locked digital video system, caused by simultaneous locking of the genlock device causing continuous changing of a shared clock signal, by calculating a time shift occurring in an output waveform, converting the time shift into an equivalent phase shift and sending a corresponding phase correction number to a waveform generator block to correct the time shift, and thus stabilize subcarrier generation.

45 Claims, 5 Drawing Sheets



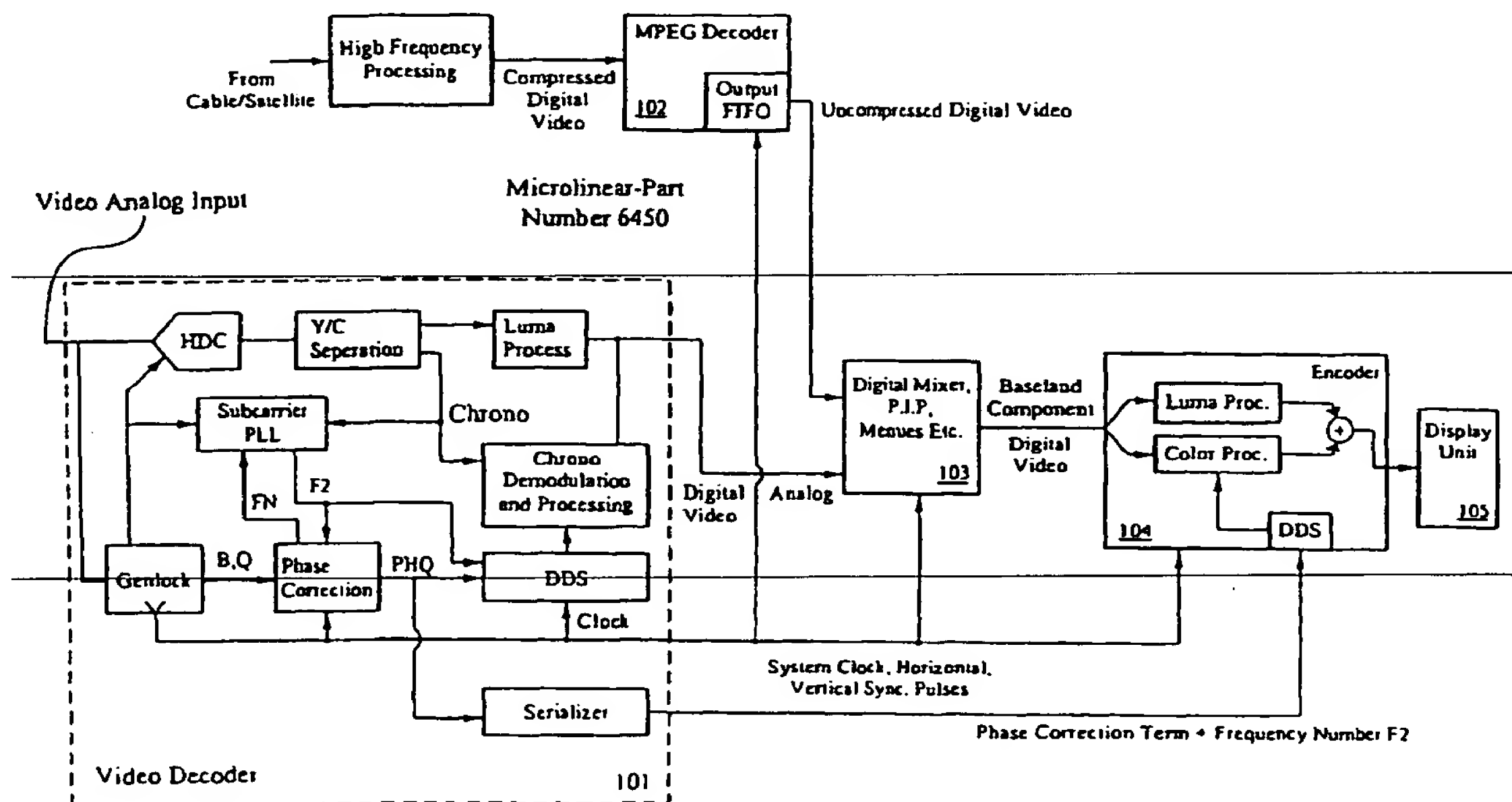


Fig. 1

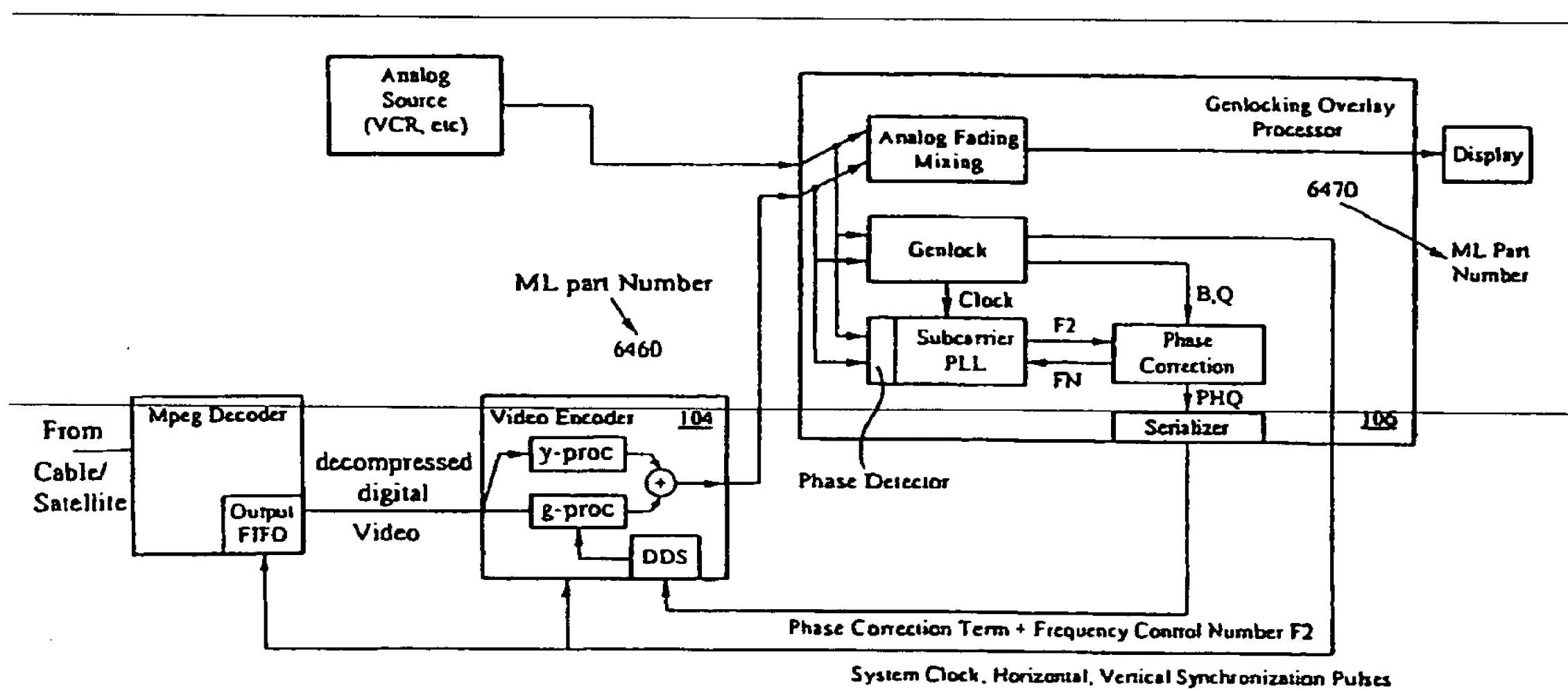
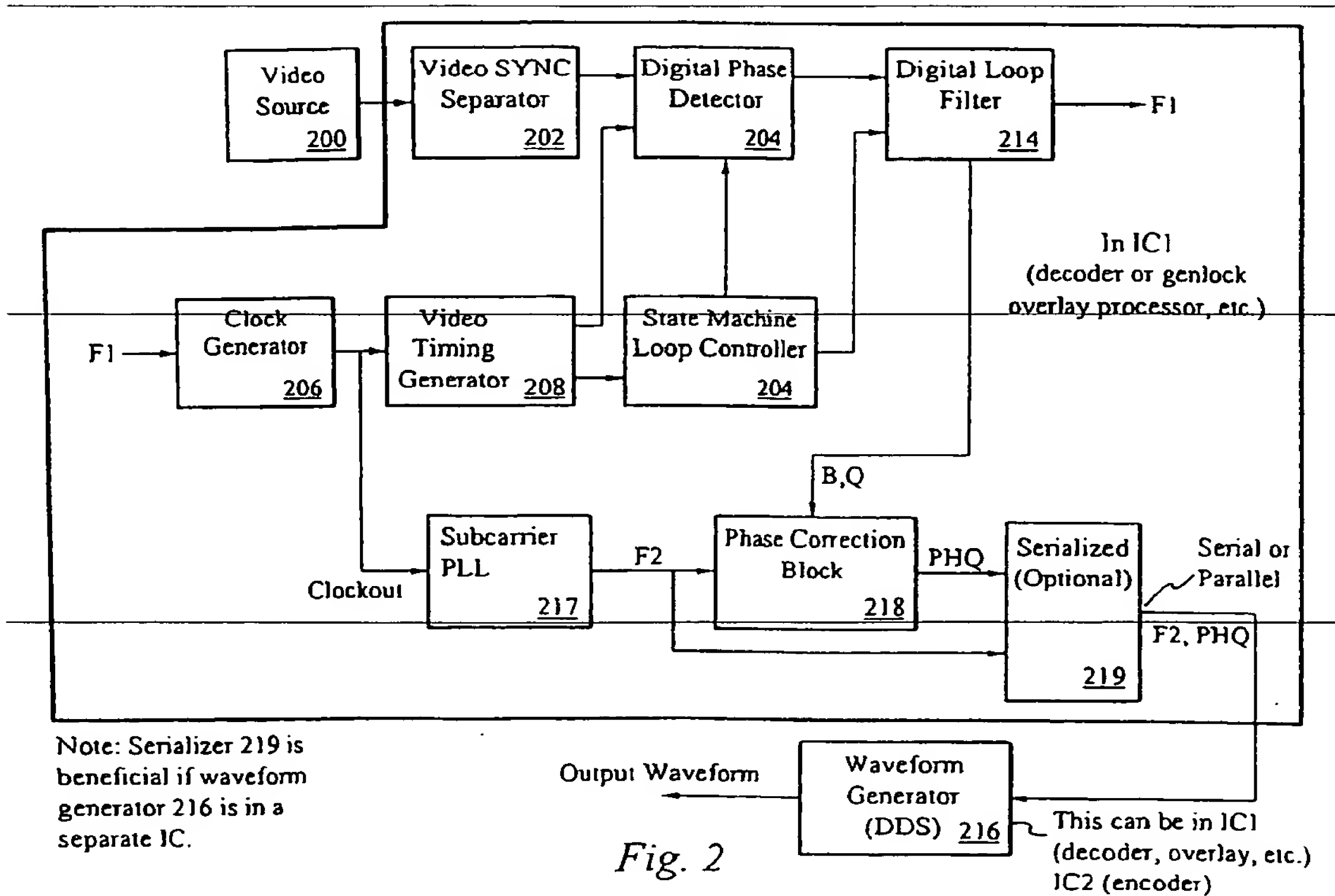


Fig. 1a

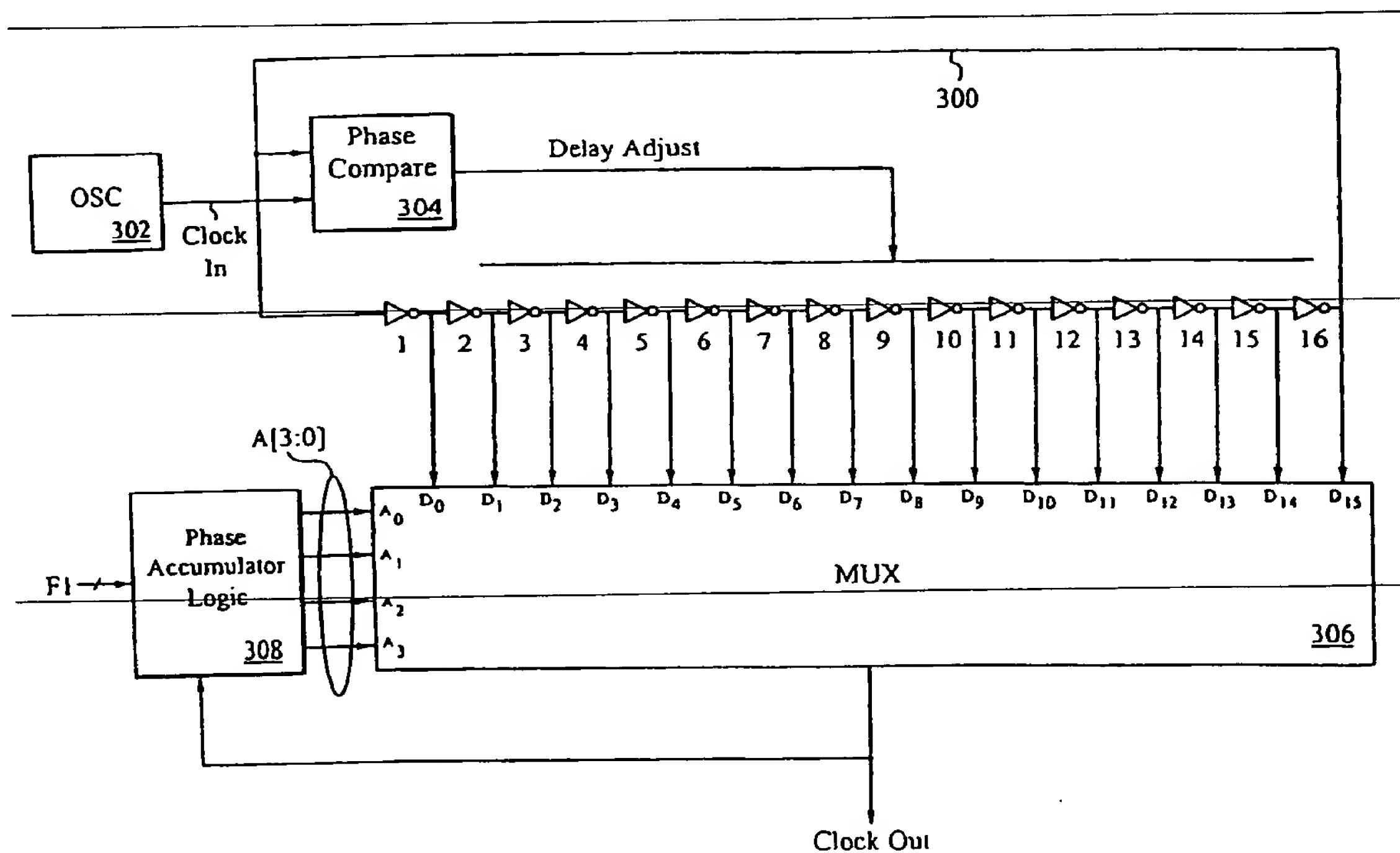


U.S. Patent

May 25, 2004

Sheet 4 of 5

6,741,289 B1



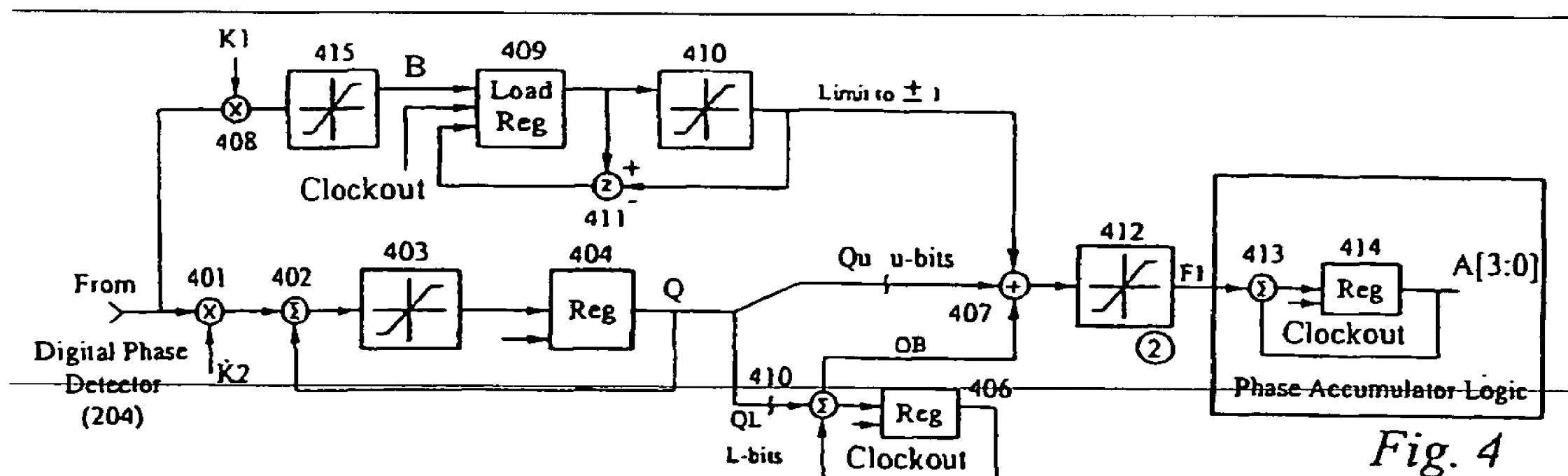


Fig. 4

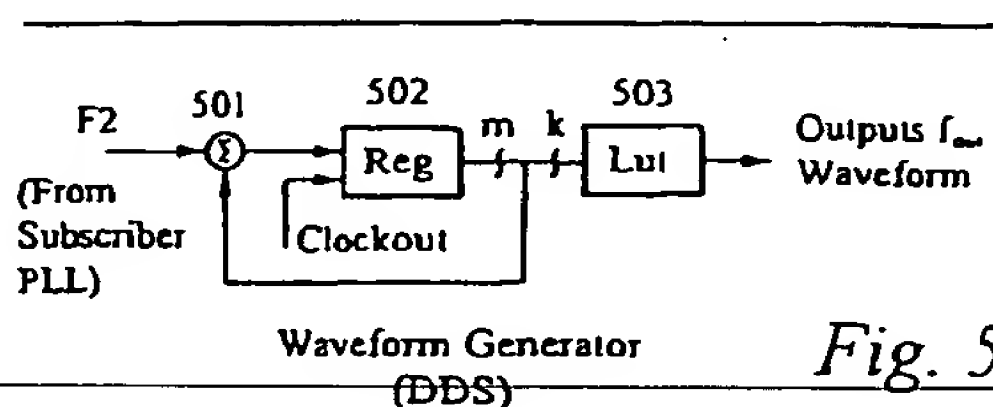


Fig. 5

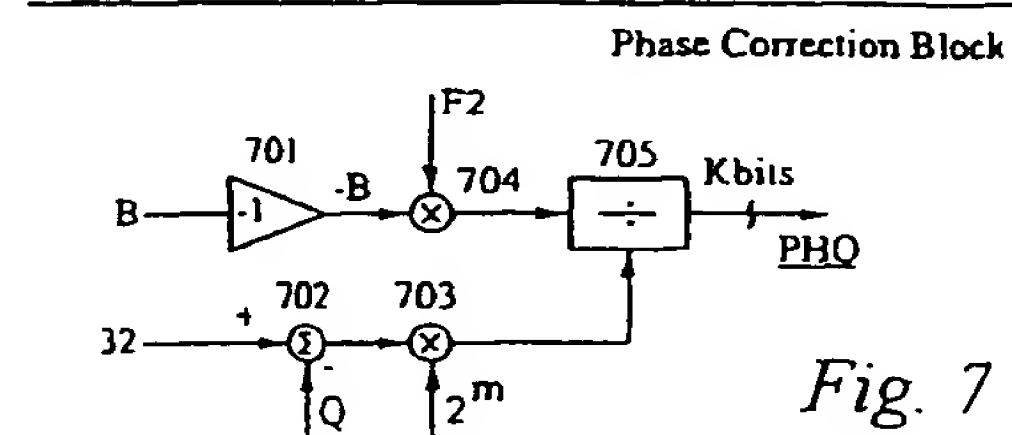


Fig. 7

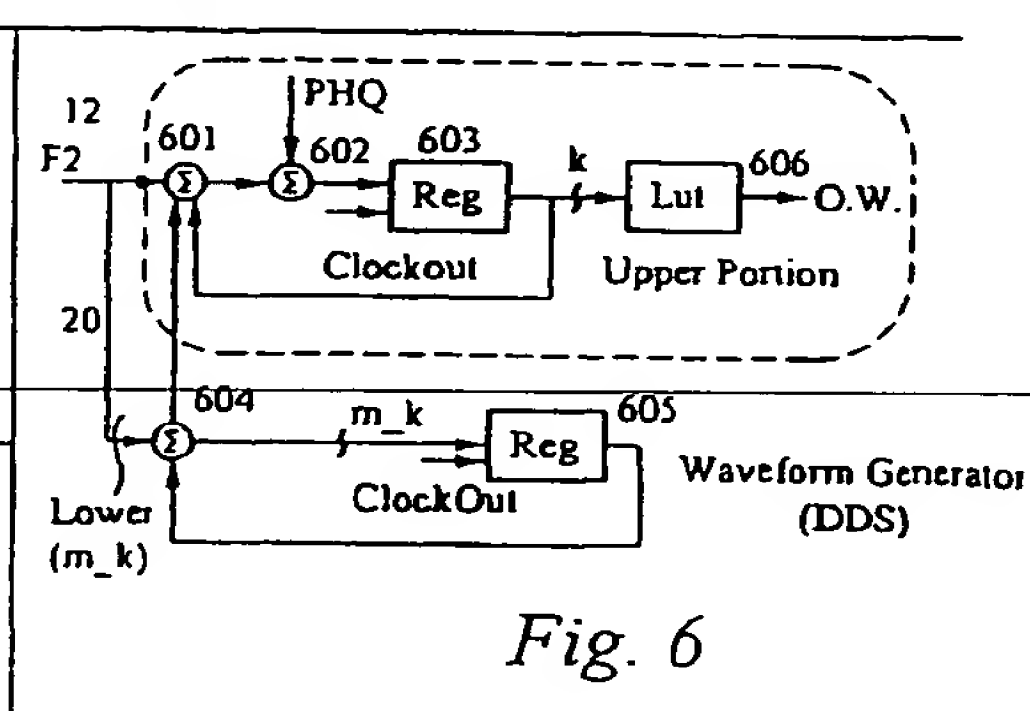


Fig. 6